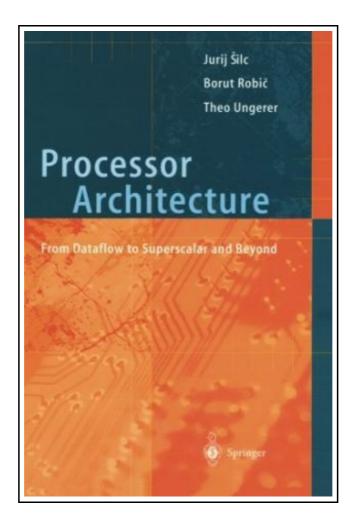
Processor Architecture



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Reviews

Undoubtedly, this is the best function by any writer. This really is for those who statte there was not a really worth reading. Its been written in an exceptionally basic way which is merely right after i finished reading through this book by which really transformed me, change the way i really believe. (Dr. Deonte Hammes DDS)

PROCESSOR ARCHITECTURE



Book Condition: New. Publisher/Verlag: Springer, Berlin | From Dataflow to Superscalar and Beyond | Die Autoren beschreiben die Prinzipien der Prozessor-Architektur. Die verschiedenen Prozessortypen werden Beispielen ausführlich dargestellt. Die Vorstellung neuester Implementierungsmethoden für künftige Mikroprozessorarchitekturen runden das Buch ab. This monograph surveys architectural mechanisms and implementation techniques for exploiting fine-grained and coarsegrained parallelism within microprocessors. It starts with a review of past techniques, continues with a comprehensive account of state-of-the-art techniques used in microprocessors that covers both the concepts involved and implementations in sample processors, and ends with a thorough review of the research techniques that will lead to future microprocessors. | A survey of architectural mechanisms and implementation techniques for exploiting fine- and coarse-grained parallelism within microprocessors. Beginning with a review of past techniques, the monograph provides a comprehensive account of state-of-theart techniques used in microprocessors, covering both the concepts involved and implementations in sample processors. The whole is rounded off with a thorough review of the research techniques that will lead to future microprocessors. XXXXXXX Neuer Text This monograph surveys architectural mechanisms and implementation techniques for exploiting fine-grained and coarse-grained parallelism within microprocessors. It presents a comprehensive account of state-of-the-art techniques used in microprocessors that covers both the concepts involved and possible implementations. The authors also provide application-oriented methods and a thorough review of the research techniques that will lead to the development of future processors. | '1. Basic Pipelining and Simple RISC Processors.- 1.1 The RISC Movement in Processor Architecture.- 1.2 Instruction Set Architecture.- 1.3 Examples of RISC ISAs.- 1.4 Basic Structure of a RISC Processor and Basic Cache MMU Organization.- 1.5 Basic Pipeline Stages.- 1.6 Pipeline Hazards and Solutions.- 1.6.1 Data Hazards and Forwarding.- 1.6.2 Structural Hazards.- 1.6.3 Control Hazards, Delayed Branch Technique, and Static Branch Prediction.- 1.6.4 Multicycle Execution.- 1.7 RISC...



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